

<u>Title:</u>	<i>Basic Back Side Circuit Modification (FIB Circuit Edit)</i>
<u>Training duration:</u>	<i>5 days</i>
<u>Training cost:</u>	<i>Groups of 1 – 3 trainees, US\$6000 per group</i>
<u>Background required:</u>	<i>Basic Front Side Circuit Modification training, or equivalent knowledge</i>
<u>Student Objective:</u>	<i>Gain ability to independently perform backside sample navigation, local Silicon thinning, cut and connection on Flip-Chip packaged device.</i>
<u>Equipment:</u>	<i>Micrion 986FC, Vectra 986(+), VectraVision</i>
<u>Syllabus:</u>	<i>Available at www.partbeamsystech.com/training.html</i>