

<b><u>Title:</u></b>	<i>FIB Circuit Edit Design and Planning of the Circuit Modification Procedure</i>
<b><u>Training duration:</u></b>	<i>2 days</i>
<b><u>Training cost:</u></b>	<i>Groups of 1 – 5 trainees, US\$2400 per group</i>
<b><u>Background required:</u></b>	<i>Both “Basic FIB Circuit Modification - Front Side” and “Basic FIB Circuit Modification – Backside” training or equivalent knowledge</i>
<b><u>Student Objective:</u></b>	<i>Know and understand major components and layers of modern CMOS microelectronic circuits, basics of “FIB-friendly” circuit layout, limitations of FIB circuit editing process, rules of successful circuit modification..</i>
<b><u>Equipment:</u></b>	<i>Micrion 2000, 2500, 9000, 9100, 986, 986FC, Vectra 986(+), VectraVision,</i>
<b><u>Syllabus:</u></b>	<i>Available online: <a href="http://www.partbeamsystech.com/training">www.partbeamsystech.com/training</a></i>