

FIB Circuit Edit Design and Planning of the Circuit Modification Procedure
Abbreviated* Syllabus.

1. Layers and materials of modern CMOS circuitry - overview
2. CAD representation
3. Microelectronics materials and FIB processes
4. Limitations of FIB circuit modification
5. “Rules of Design” for successful Circuit Edit
6. Flowchart creation for Circuit Modification process
7. Practical Training: circuit modification design for front and back side samples

*Detailed syllabus is available upon request sent to: info@partbeamsystech.com